Notice of References Cited

Application/Control No.

O9/683,351

Examiner

Kevin P Rizzuto

Applicant(s)/Patent Under Reexamination LEBER ET AL.

Art Unit
Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,493,819 B1	12-2002	Mahurin et al.	712/210
	В	US-5,574,928 A	11-1996	White et al.	712/23
	C	US-6,449,710 B1	09-2002	Isaman, David L.	712/216
	D	US-5,752,062	05-1998	Gover et al.	714/37
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	ı	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	s					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	υ´	Johnson, Mike. Superscalar Microprocessor Design. Prentice-Hall, Inc., 1991. Pages 133-134.
*	V	Hennessy, John and Patterson, David. Computer Organization and Design, 2 nd ed. Morgan Kaufmann Publishers, Inc., 1998. Pages 118, 119, 175, 384 and B-9.
	w	
	×	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.